


<b>Application Number</b> 	<b>Application No.</b> 10/659,637	<b>Applicant(s)</b> WONG ET AL.	

<b>TERMINAL DISCLAIMER</b> <b>filed 10/18/04 has been:</b>	<input type="checkbox"/> <b>APPROVED</b>	<input checked="" type="checkbox"/> <b>DISAPPROVED</b>
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Patent**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of:

Wong et al.

Serial No.: 10/659,637

Filing Date: September 9, 2003

For: VOLTAGE ID BASED FREQUENCY  
CONTROL FOR CLOCK  
GENERATING CIRCUIT

Examiner: Luu, An T.

Art Unit: 2816

**TERMINAL DISCLAIMER****DISAPPROVED**

James Y. Go not attorney

OCT 29 2004  
Not attorney of record  
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The undersigned attorney represents that the undersigned attorney is the attorney of record for the above-referenced patent application.

The assignee of the entire right, title, and interest in and to the above-referenced patent application is Intel Corporation ("assignee"),  
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a Delaware corporation having a place of business at  
(State of Incorporation)

2200 Mission College Blvd., Santa Clara, California 95052  
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Ser. No. 10/659,637  
Filing Date: September 9, 2003

- 1 -

Examiner: Luu, An T.  
Art Unit: 2816

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02-0457

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The terminal part of any patent granted on the above-identified application that would extend beyond the expiration of the full statutory term of

X United States Patent Application No. 10/136,321, entitled  
VOLTAGE ID BASED FREQUENCY CONTROL FOR CLOCK  
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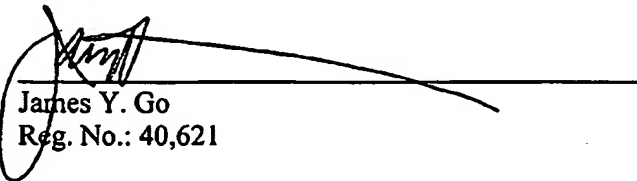
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Please charge Deposit Account No. 02-2666 for any fee deficiency that may be due. A duplicate of this Terminal Disclaimer is enclosed for Deposit Account charging purposes.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 10-14-04

  
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